

**PATENT** 



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Nils Gura and Hans Eberle

Assignee:

Sun Microsystems, Inc.

Certificate

MAY 1 3 2005

Title:

LEAST CHOICE FIRST ARBITER

of Correction

Patent No.:

6,882,649

Issued:

April 19, 2005

Atty. Docket No.: 004-4282

May 6, 2005

ATTN: Certificate of Correction Branch COMMISSIONER FOR PATENTS P.O. Box 1450

Alexandria, VA 22313-1450

## REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT – PTO MISTAKE (37 C.F.R. § 1.322(a))

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

- 1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
- 2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file: Page 1, line 2; page 8, line 8 and line 9; page 12, line 23; page 14, line 31; page 15, line 21; page 18, line 3; and page 19, line 37.
- 3. Please send the Certificate to:

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Respectfully submitted,

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,882,649 B1
DATED : April 19, 2005

INVENTOR(S): Nils Gura and Hans Eberle

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 5: "This copyright" should read -- The copyright --.

Column 5

Line 33: "Schedule cycle 0: [1,0]," should read -- Schedule cycle 0: [0,0], --. Line 34: "Schedule cycle 2: [1,0]," should read -- Schedule cycle 1: [1,0], --.

Column 8, Line 16: "R[I+res, J+res]=I" should read -- R[I+res, J+res]=1 --.

Column 9, Line 45: "input port and input port" should read -- input port 1 and input port --.

Column 10, Line 8: "i(i=0...n-1):" should read -- i(i=0..n-1): --.

Column 11, Line 21: "J: 0..MaxReq-1;" should read -- J: 0..MaxRes-1; --.

Column 12, Line 60: "mod MaxReq:" should read -- mod MaxReq; --.

MAILING ADDRESS OF SENDER: ZAGORIN O'BRIEN GRAHAM LLP 7600B N. Capital of Texas Hwy., Suite 350 Austin, Texas 78731 PATENT NO. 6,882,649 B1